EXAMINED BY : Yung Chang Hu	EMERGING DISPLAY TECHNOLOGIES CORPORATION	FILE NO . CAS-51629
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APPROVED BY: David Chang		TOTAL PAGE : 23
		VERSION : 2

CUSTOMER ACCEPTANCE SPECIFICATIONS

MODEL NO. :

ET070000DM6
(RoHS)

FOR MESSRS :

CUSTOMER'S APPROVAL

DATE :

BY :

EMERGING DISPLAY TECHNOLOGIES CORPORATION	MODEL NO.	VERSION	PAGE
	ET0 70000DM6 (RoHS)	2	0-1

RECORDS OF REVISION

DOC . FIRST ISSUE

JUL.18, 2007

DATE	REVISED PAGE NO.	SUMMARY
AUG.27 ,2007	1	2. MECHANICAL SPECIFICATIONS (3) MODULE SIZE：165W * 104H *10.5D mm (WITHOUT FPC) → (3) MODULE SIZE：165W * 104H *10D mm (WITHOUT FPC)
	6	6.1 OPTICAL CHARACTERISTICS ITEM / SYMBOL / CONDITION / MIN. / TYP. / MAX. / UNIT / REMARK VIEWING ANGLE: θ_{x+}, θ_{x-}, θ_{y+}, θ_{y-} — CR ≥ 10 — (θ_x=0°: θ_{x+}, θ_{x-}; θ_y=0°: θ_{y+}, θ_{y-}) — (40) / (60) / (60) / (60) — deg. — (2) / (3) ↓ ITEM / SYMBOL / CONDITION / MIN. / TYP. / MAX. / UNIT / REMARK VIEWING ANGLE: θ_{x+}, θ_{x-}, θ_{y+}, θ_{y-} — CR ≥ 10 — (θ_x=0°: θ_{x+}, θ_{x-}; θ_y=0°: θ_{y+}, θ_{y-}) — (60) / (60) / (70) / (70) — deg. — (2) / (3)
	8	7. OUTLINE DIMENSIONS MARK △1：MODIFY MODULE THICKNESS

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1. GENERAL SPECIFICATIONS

1.1 APPLICATION NOTES FOR CONTROLLER/DRIVER

PLEASE REFER TO :

HIMAX HX8232
HIMAX HX8643

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE)

2. MECHANICAL SPECIFICATIONS

(1)	DISPLAY SIZE (inch)	7”
(2)	NUMBER OF DOTS	800W * (RGB) * 480H DOTS
(3)	MODULE SIZE	165W * 104H *10D mm (WITHOUT FPC)
(4)	ACTIVE AREA	152.4W * 91.44H mm (LCD)
(5)	DOT SIZE	0.0635W * 0.1905H mm
(6)	PIXEL SIZE	0.1905W * 0.1905H mm
(7)	LCD TYPE	TFT , TRANSMISSIVE,ANTE-GLARE
(8)	COLOR	262K (18BIT)
(9)	VIEWING DIRECTION	6 O’CLOCK
(10)	BACK LIGHT	LED , COLOR : WHITE

3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER VOLTAGE	VCC	-0.5	+5	V	VSS=0
INPUT VOLTAGE	Vin	- 0.3	VCC+0.3	V	

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPERATING		STORAGE		REMARK
	MIN.	MAX.	MIN.	MAX.	
AMBIENT TEMPERATURE	-30°C	80°C	-40°C	80°C	NOTE (1) , (2)
HUMIDITY	NOTE (3)		NOTE (3)		WITHOUT CONDENSATION
VIBRATION	—	2.45 m/s^2 (0.25 G)	—	11.76 m/s^2 (1.2 G)	5~20Hz , 1HR 20~500Hz(20Hz) , 1HR 20~500Hz(500Hz) , 1HR X,Y,Z,TOTAL 3HR
SHOCK	—	29.4 m/s^2 (3 G)	—	490 m/s^2 (50 G)	10 m SECONDS XYZ DIRECTIONS 1 TIME EACH
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE		

NOTE (1) : Ta AT -40°C : 48HR MAX .
80°C : 168HR MAX .
NOTE (2) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE
THIS PHENOMENON IS REVERSIBLE.
NOTE (3) : Ta ≤ 60°C : 90%RH MAX (96HRS MAX).
Ta > 60°C : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C(96HRS MAX).

4. ELECTRICAL CHARACTERISTICS

Ta = 25 °C

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY	VCC	—	(2.7)	(3.3)	(3.6)	V	
POWER SUPPLY CURRENT FOR LCM	ICC	VCC = (3.3V) LED B/L = ON	—	(510)	(600)	mA	
LOW LEVEL INPUT VOLTAGE	VIL	—	0	—	0.3*VCC	V	
HIGH LEVEL INPUT VOLTAGE	VIH	—	0.7*VCC	—	VCC	V	
OUTPUT LOW VOLTAGE	VOL	IOL = -1mA	VSS	—	VSS+0.4	V	
OUTPUT HIGH VOLTAGE	VOH	IOH = 1mA	VCC-0.4	—	—	V	
FRAME FREQUENCY	fFRAME	—	—	60	90	Hz	
FORWARD VOLTAGE	V_F	(I_F=180mA)	—	(10.5)	—	V	
LED LIFE TIME	—	—	(30000)	(40000)	—	hr	

5 . TIMING CHART

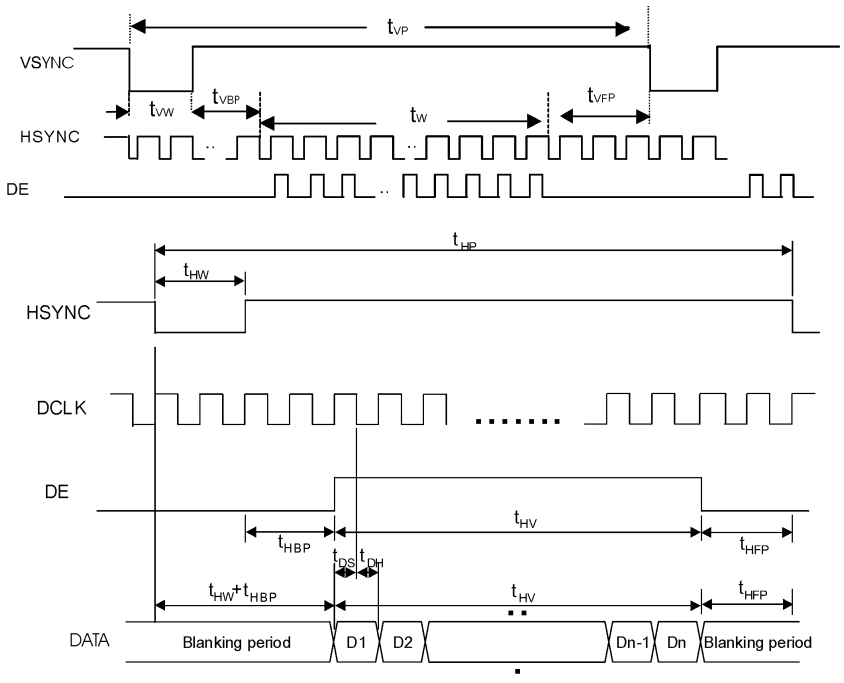
5.1 SYNC MODE SIGNAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
CLOCK PERIOD	t_{CLK}	25.0	30.0	50.0	ns
CLOCK FREQUENCY	f_{CLK}	20	33.3	40	MHz
CLOCK LOW LEVEL WIDTH	t_{WCL}	8	—	—	ns
CLOCK HIGH LEVEL WIDTH	t_{WCH}	8	—	—	ns
CLOCK RISE, FALL TIME	$t_{CLK}r$, t_{CLKf}	—	—	3	ns
HSYNC PERIOD	t_{HP}	888	928	1054	t_{CLK}
HYNC PULSE WIDTH (1)	t_{HW}	1	48	87	t_{CLK}
HYNC BACK PORCH (1)	t_{HBP}	1	40	87	t_{CLK}
HYNC WIDTH+BACK PORCH (1)	$t_{HW} + t_{HBP}$	88	88	88	t_{CLK}
HORIZONTAL VAILD DATA WIDTH	t_{HV}	800	800	800	t_{CLK}
HYNC FRONT PORCH	t_{HFP}	0	40	166	t_{CLK}
VSYNC PERIOD	t_{VP}	512	525	1056	t_{HP}
VYNC PULSE WIDTH (2)	t_{VW}	1	3	31	t_{HP}
VSYNC BACK PORCH (2)	t_{VW}	1	29	31	t_{HP}
VYNC WIDTH + BACK PORCH (2)	$t_{VW} + t_{VW}$	32	32	32	t_{HP}
VERICAL VALID DATA WIDTH	t_W	480	480	480	t_{HP}
VSYNC FRONT PORCH	t_{VFP}	0	13	544	t_{HP}
DATA SETUP TIME	t_{DS}	5	—	—	ns
DATA HOLD TIME	t_{DH}	10	—	—	ns

NOTE :

(1) (HYNC PULSE WIDTH + HYNC BACK PORCH) MUST BE 88 t_{CLK}.

(2) (VYNC PULSE WIDTH + VYNC BACK PORCH) MUST BE 32 t_{HP}.

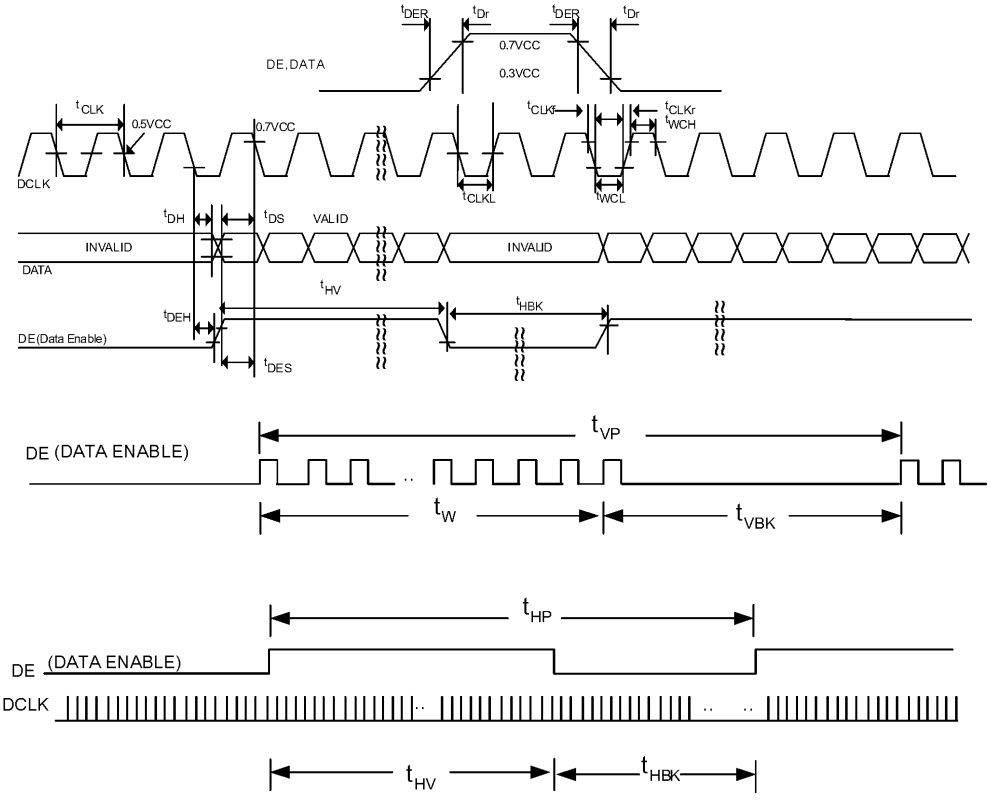


5.2 DE MODE SIGNAL CHARACTERISTICS

ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK	PERIOD	t_{CLK}	25.0	30.0	50.0	ns
	FREQUENCY	f_{CLK}	20	33.3	40	MHz
	LOW LEVEL WIDTH	t_{WCL}	8	—	—	ns
	HIGH LEVEL WIDTH	t_{WCH}	8	—	—	ns
	RISE, FALL TIME	$t_{CLK}r$, t_{CLKf}	—	—	3	ns
	DUTY (1)	—	888	928	1054	t_{CLK}
DE (DATA ENABLE)	SETUP TIME	t_{DES}	1	48	87	t_{CLK}
	HOLD TIME	t_{DEH}	1	40	87	t_{CLK}
	RISE, FALL TIME	$t_{DEr} + t_{DEf}$	88	88	88	t_{CLK}
	HORIZONTAL PERIOD	t_{HP}	800	800	800	t_{CLK}
	HORIZONTAL VALID	t_{HV}	0	40	166	t_{CLK}
	HORIZONTAL BLANK	t_{HBK}	512	525	1056	t_{HP}
	VERTICAL PERIOD	t_{VP}	1	3	31	t_{HP}
	VERTICAL VALID	t_W	1	29	31	t_{HP}
	VERTICAL BLANK	t_{VBK}	32	32	32	t_{HP}
DATA	SETUP TIME	t_{DS}	480	480	480	t_{HP}
	HOLD TIME	t_{DH}	0	13	544	t_{HP}
	RISE,FALL TIME	$t_{Dr} + t_{Df}$	5	—	—	ns

NOTE :

(1) t_{CLK} / t_{CLK}



6. OPTICAL CHARACTERISTICS (NOTE1)

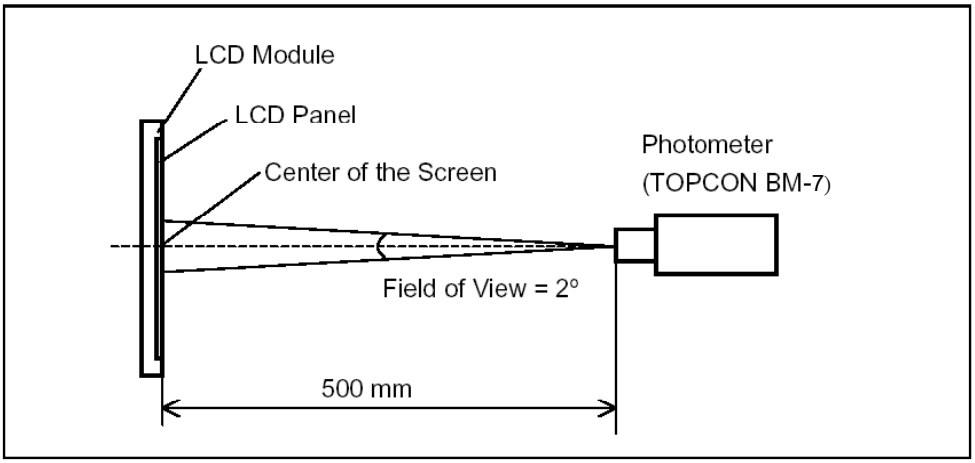
6.1 OPTICAL CHARACTERISTICS

Ta = 25 ± 2 °C

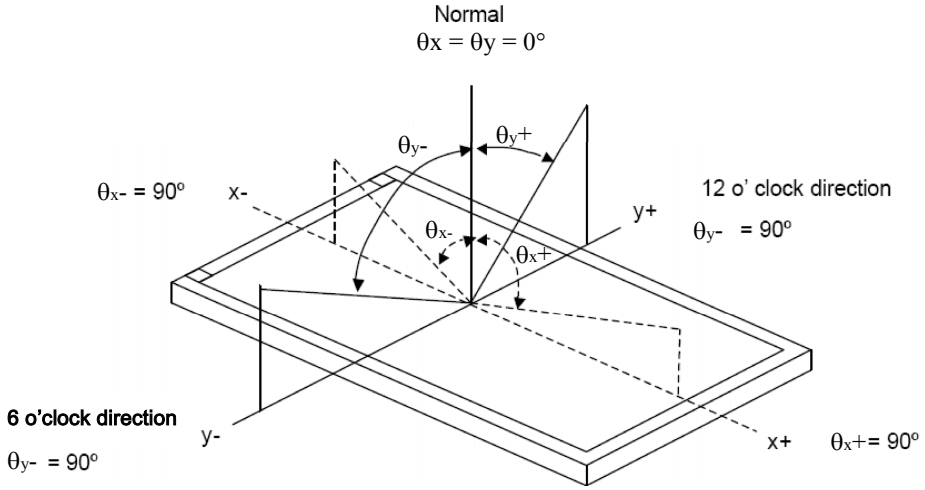
ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT	REMARK
VIEWING ANGLE		θ_{y+}	CR ≥ 10	θ_x=0°	—	(60)	—	deg.	(2) (3)
		θ_{y-}			—	(60)	—		
		θ_{x+}		θ_y=0°	—	(70)	—		
		θ_{x-}			—	(70)	—		
CONTRAST RATIO		CR	θx=0° , θy=0°		(300)	(400)	—	—	(3)
RESPONSE TIME		t r (rise)	θx=0° , θy=0°		—	TBD	—	msec	(4)
		t f (fall)			—	TBD	—		
THE BRIGHTNESS OF MODULE		B	θx=0° , θy=0° (IF = 180mA)		(350)	(450)	—	cd/m2	(5)
COLOR OF CIE COORDINATE	WHITE	x	θx=0° , θy=0° (IF = 180 mA) (NTSC : 45 %)		(0.26)	(0.31)	(0.36)	—	(6)
		y			(0.29)	(0.34)	(0.39)		
	RED	x			(0.56)	(0.61)	(0.66)	—	
		y			(0.30)	(0.35)	(0.40)		
	GREEN	x			(0.26)	(0.31)	(0.36)	—	
		y			(0.49)	(0.54)	(0.59)		
	BLUE	x			(0.09)	(0.14)	(0.19)	—	
		y			(0.11)	(0.16)	(0.21)		
THE UNIFORMITY OF MODULE		—	—		75	80	—	%	—

NOTE (1) : TEST EQUIPMENT SETUP :

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES , THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE , WINDLESS , AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 2° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



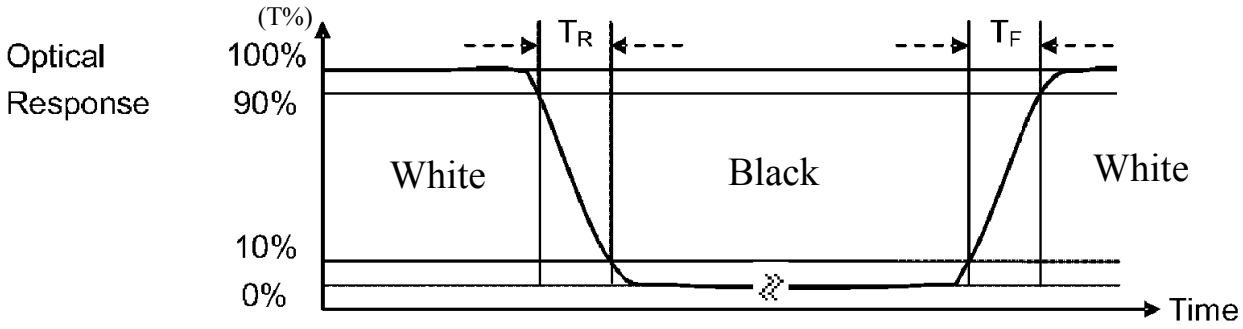
NOTE (2) : DEFINITION OF VIEWING ANGLE :



NOTE (3) : DEFINITION OF CONTRAST RATIO :

$$\text{CONTRASTRATIO(CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$$

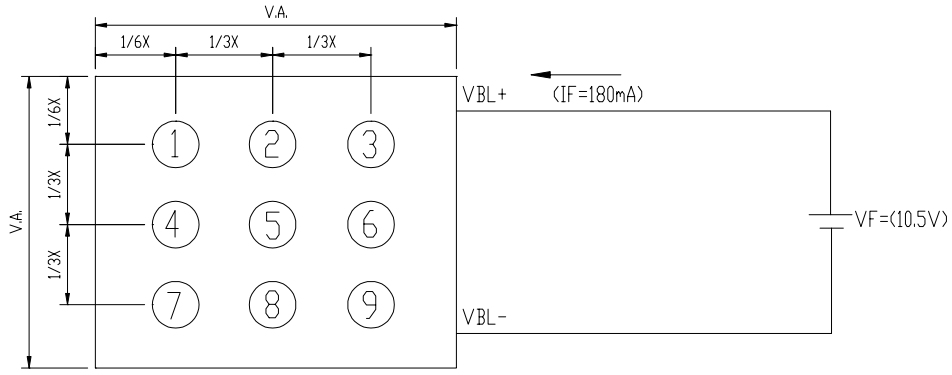
NOTE (4) : DEFINITION OF RESPONSE TIME : TR AND TF
THE FIGURE BVELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5) : BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"

NOTE (6) : THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY

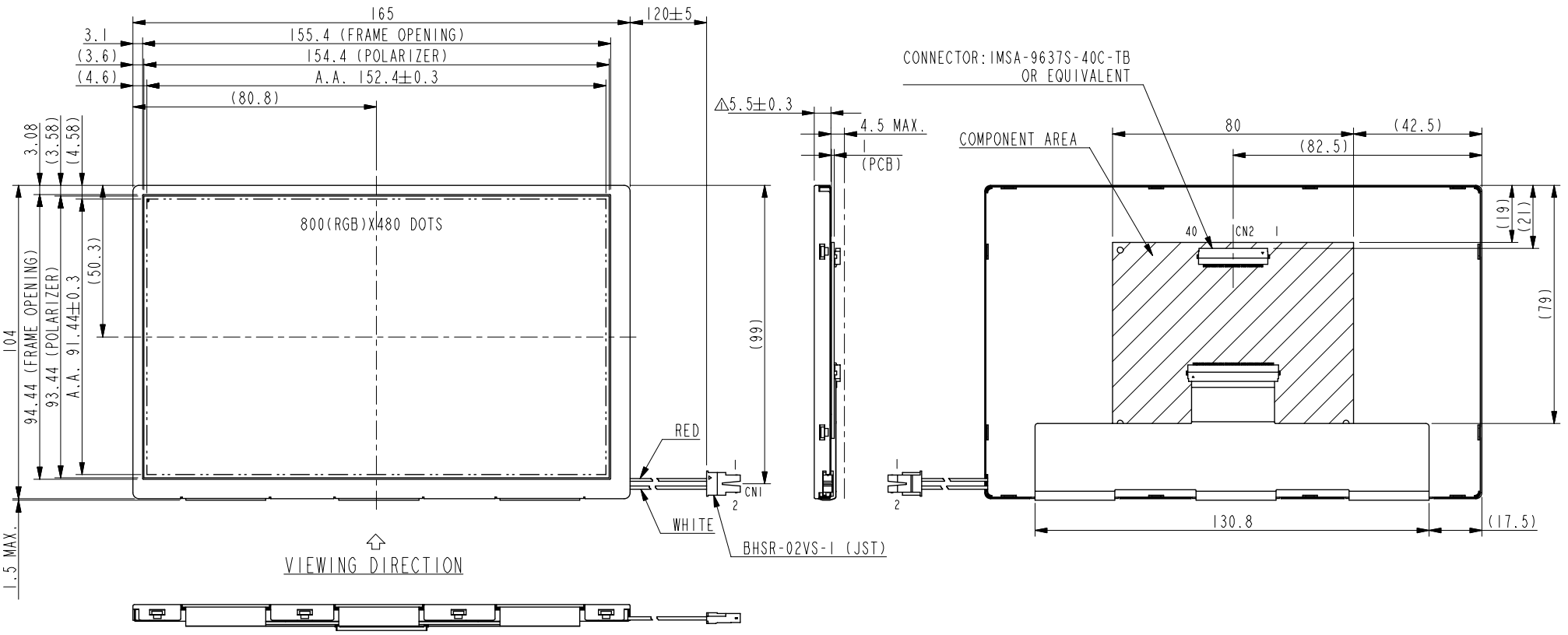


UNIT : mm

6.3 THE CALCULATING METHOD OF UNIFORMITY

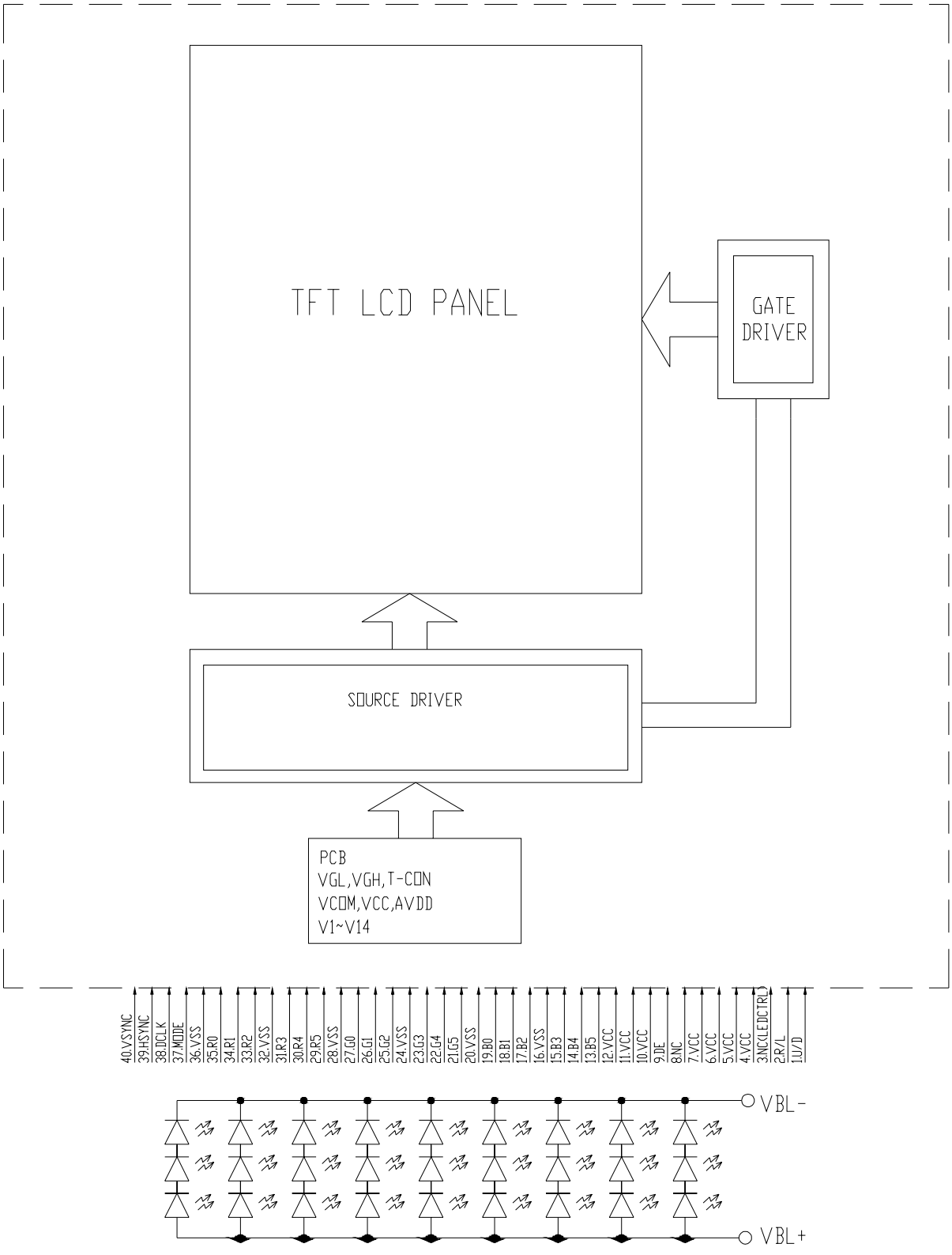
$$\text{UNIFORMITY}: \left[1 - \frac{\text{MAXIMUN BRIGHTESS} - \text{MINIMUN BRIGHTESS}}{\text{AVERAGE BRIGHTESS}}\right] \times 100\%$$

7. OUTLINE DIMENSIONS

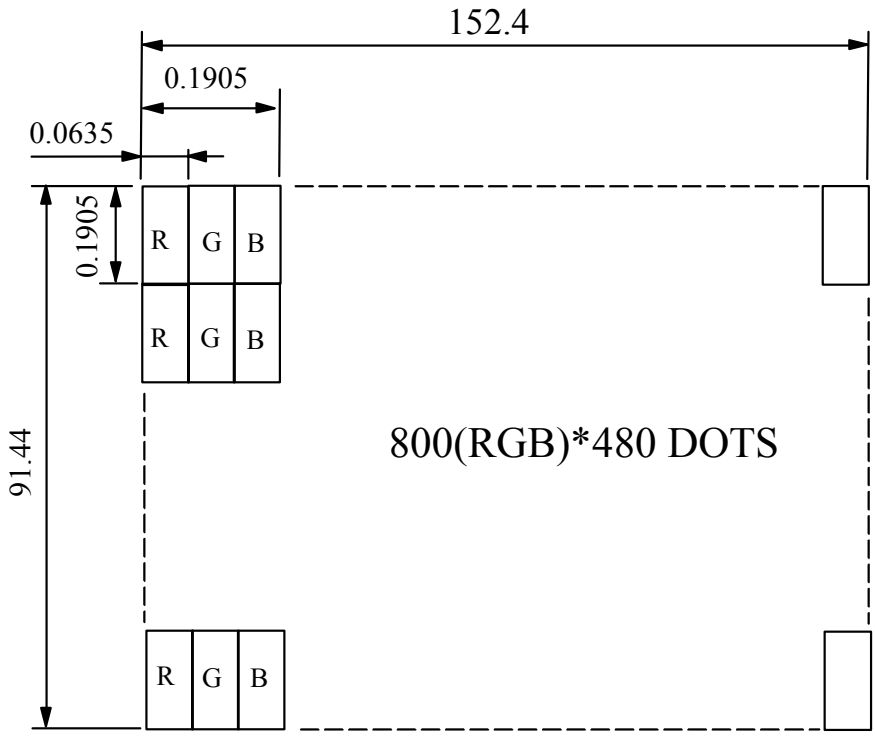


UNIT : mm
SCALE : NTS
NOT SPECIFIED TOLERANCE IS ± 0.5
NOTE : MARK ⚠ MODIFY (NUMBER NOTE MODIFY VERSION)

8. BLOCK DIAGRAM



9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm
SCALE : NTS
NOT SPECIFIED TOLERANCE IS ± 0.1
DOTS MATRIX TOLERANCE IS ± 0.01

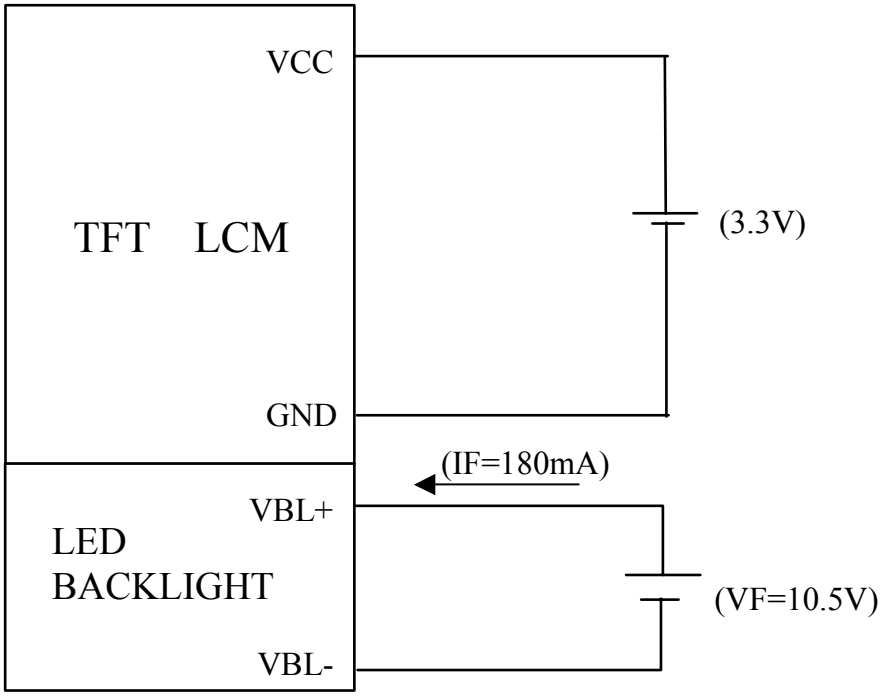
10. INTERFACE SIGNALS

PIN NO	SYMBOL	I/O	FUNCTION
1	U/D	I	U/D=H:OUT1→OUT2→ ------- →OUT480 U/D=L:OUT480→ ------- →OUT2→ OUT1
2	R/L	I	R/L= H:OUT1→OUT2→ ------- →OUT840 R/L=L:OUT840→ ------- →OUT2→ OUT1
3	NC (LEDCTRL)	—	NC
4	VCC	P	POWER SUPPLY (3.3V)
5	VCC	P	POWER SUPPLY (3.3V)
6	VCC	P	POWER SUPPLY (3.3V)
7	VCC	P	POWER SUPPLY (3.3V)
8	NC	—	NC
9	DE	I	DATA ENABLE INPUT
10	VSS	P	GROUND
11	VSS	P	GROUND
12	VSS	P	GROUND
13	B5	I	BLUE DATA BIT5
14	B4	I	BLUE DATA BIT4
15	B3	I	BLUE DATA BIT3
16	VSS	P	GROUND
17	B2	I	BLUE DATA BIT2
18	B1	I	BLUE DATA BIT1
19	B0	I	BLUE DATA BIT0
20	VSS	P	GROUND
21	G5	I	GREEN DATA BIT 5
22	G4	I	GREEN DATA BIT 4
23	G3	I	GREEN DATA BIT 3
24	VSS	P	GROUND
25	G2	I	GREEN DATA BIT 2
26	G1	I	GREEN DATA BIT 1
27	G0	I	GREEN DATA BIT 0
28	VSS	P	GROUND
29	R5	I	RED DATA BIT 5
30	R4	I	RED DATA BIT 4

31	R3	I	RED DATA BIT 3
32	VSS	P	GROUND
33	R2	I	RED DATA BIT 2
34	R1	I	RED DATA BIT 1
35	R0	I	RED DATA BIT 0
36	VSS	P	GROUND
37	MODE	I	DE/SYNC MODE SELECT,NORMALLY PULLED HIGH. H:DE MODE L:SYNC MODE
38	DCLK	I	DOT DATA COLOCK
39	HSYNC	I	HORIZONTAL SYNC INPUT
40	VSYNC	I	VERTICAL SYNC INPUT

11. POWER SUPPLY

11.1 POWER SUPPLY FOR LCM



12. INSPECTION CRITERION

12.1 APPLICATION

This inspection standard is to be applied to the LCD module delivered from EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) to customers

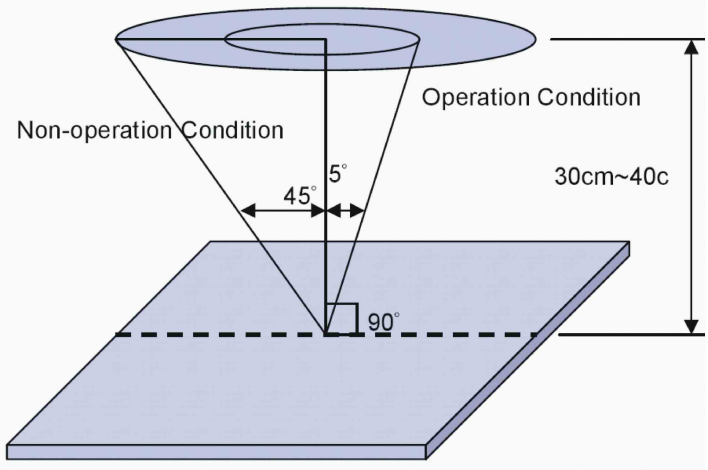
12.2 INSPECTION CONDITIONS

12.2.1 (1)Observation Distance : 35cm±5cm

(2)View Angle :

Non-operation Condition : ±5°(perpendicular to LCD panel surface)

Operation Condition : ±45° (perpendicular to LCD panel surface)



12.2.2 Environment Conditions :

Ambient Temperature		20°C~25°C
Ambient Humidity		65±20%RH
Ambient Illumination	Cosmetic Inspection	More than 600Lux
	Functional Inspection	300~500 Lux

12.2.3 Inspection lot

Quantity per delivery lot for each model

12.2.4 Inspection method

A sampling inspection shall be made according to the following provisions to judge The acceptability

(a)Applicable standard : MIL-STD-105E

Normal inspection , single sampling

Level Ⅱ

(b)AQL : Major defect : AQL 0.65%

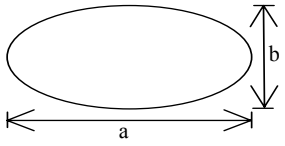
Minor defect : AQL 1.0%

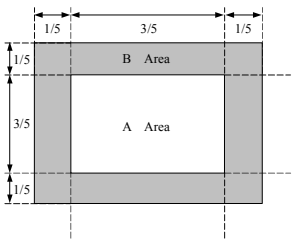
12.3 INSPECTION STANDARDS

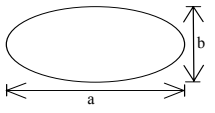
12.3.1 VISUAL DEFECTS CLASSIFICATION

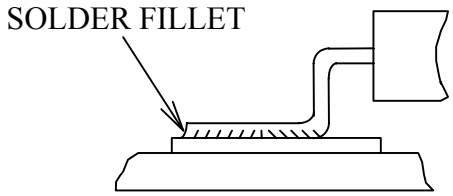
TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
MAJOR DEFECT	1.DISPLAY ON	• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
	2.BACKLIGHT	• NO LIGHT • FLICKERING AND OTHER ABNORMAL ILLUMINATION	
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
MINOR DEFECT	1.DISPLAY ZONE	• BLACK/WHITE SPOT • BUBBLES ON POLARIZER • NEWTON RING • BLACK/WHITE LINE • SCRATCH • CONTAMINATION • LEVER COLOR SPREED	1.0
	2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	
	3.SOLDERING	• INSUFFICIENT SOLDER • SOLDERED IN INCORRECT POSITION • CONVEX SOLDERING SPOT • SOLDER BALLS • SOLDER SCRAPS	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

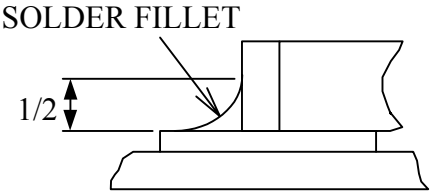
12.3.2 MODULE DEFECTS CALSSIFICATION

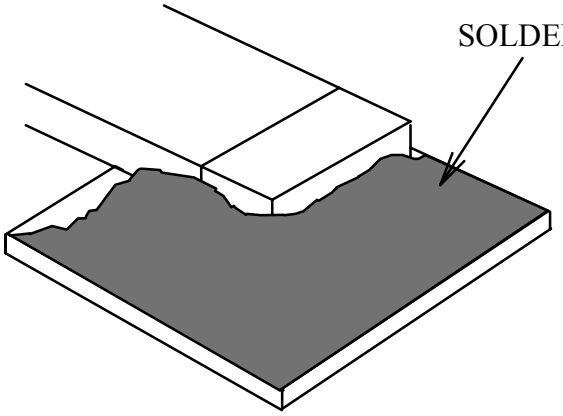
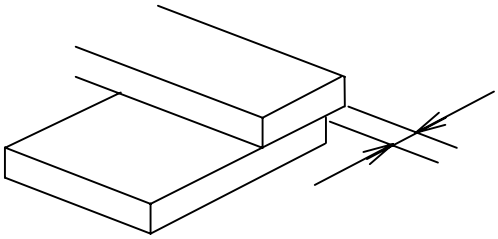
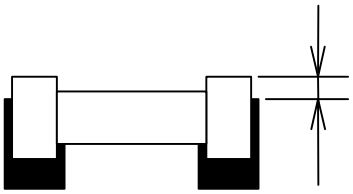
<table>
<tr><th>NO.</th><th>ITEM</th><th>CRITERIA</th></tr>
<tr><td>1.</td><td>DISPLAY ON INSPECTION</td><td>(1)INCORRECT PATTERN
(2)MISSING SEGMENT
(3)DIM SEGMENT
(4)OPERATING VOLTAGE BEYOND SPEC</td></tr>
<tr><td>2.</td><td>OVERALL DIMENSIONS</td><td>(1)OVERALL DIMENSION BEYOND SPEC</td></tr>
<tr><td>3.</td><td>BLACK SPOTS, FOREIGN MATTER, AND WHITE SPOTS (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)</td><td>(1) SPOTS
<table>
<tr><th>AVERAGE DIAMETER (mm): D</th><th>NUMBER OF PIECES PERMITTED</th><th>MINIMUM SPACE</th></tr>
<tr><td>D≤0.2</td><td>IGNORE</td><td>—</td></tr>
<tr><td>0.2<D≤0.4</td><td>5</td><td>10 mm</td></tr>
<tr><td>0.4<D</td><td>0</td><td></td></tr>
</table>
NUMBER OF TOTAL PIECES IS TO BE SET WITHIN 5 PIECES.
NOTE :
THAT WHEN THERE ARE 2 PIECES OR MORE, THEY ARE NOT TO BE CONSIDERED AS CONCENTRATED.
(2) BLURRY SPOTS (WHEN FULLY POWERED-ON)
<table>
<tr><th>AVERAGE DIAMETER (mm): D</th><th>NUMBER OF PIECES PERMITTED</th></tr>
<tr><td>D≤0.3</td><td>IGNORE</td></tr>
<tr><td>0.3<D≤0.75</td><td>5</td></tr>
<tr><td>0.75<D</td><td>0</td></tr>
</table>
Note ：Diameter D=(a+b)/2
 

NUMBER OF TOTAL PIECES IS TO BE SET WITHIN 5 PIECES.
NOTE :
THAT WHEN THERE ARE 2 PIECES OR MORE, THEY ARE NOT TO BE CONSIDERED AS CONCENTRATED.</td></tr>
<tr><td>4.</td><td>BLACK LINE
WHITE LINE
NON-DISPLAY</td><td>(1)THE BLACK LINE, WHITE LINE ARE WITHIN THE VIEWING AREA. IT IS NOT ALLOW.</td></tr>
<tr><td>5</td><td>BLACK LINE WHITE LINE ON-DISPLAY</td><td>(1) THE FOLLOWING BLACK LINE , WHITE LINE ARE WITHIN THE VIEWING AREA. WIDTH :Wmm , LENGH :Lmm
<table>
<tr><th>LENGTH : L</th><th>WIDTH : W</th><th>PERMISSIBLE NO.</th></tr>
<tr><td>L ≤ 0.5</td><td>W ≤ 0.1</td><td>IGNORE</td></tr>
<tr><td>0.5 < L ≤ 2.5</td><td>0.1 < W ≤ 0.3</td><td>4</td></tr>
<tr><td>2.5 < L</td><td>0.3 ≤ W</td><td>NONE</td></tr>
</table></td></tr>
<tr><td>6.</td><td>SCRATCHES AND DENT ON GLASS POLARIZER</td><td>(1) PLS REFER TO THE ABOVE NO.3 AND 4 TO DETERMINE SCRATCHES AND DENT ON POLARIZER OR GLASS</td></tr>
<tr><td>7.</td><td>DOT DEFECT ON DISPLAY</td><td>Judgment Criteria
<table>
<tr><th>Area</th><th>Bright Dot</th><th>Dark Dot</th><th>Total</th></tr>
<tr><td>A</td><td>3</td><td>3</td><td>4</td></tr>
<tr><td>B</td><td>5</td><td>5</td><td>5</td></tr>
</table>
(1)It is defined as Point Defect if defect area>0.5dot
(2)It is ignored if defect area≤0.5dot
(3)Weak point defect will be defined as Bright Dot if it can be observed through ND filter 6%
(4)The distance between 2 dot defect≥5mm
(5)Not Allowed Joint point defect
Note : A/B Area Definition
 
</td></tr>
</table>

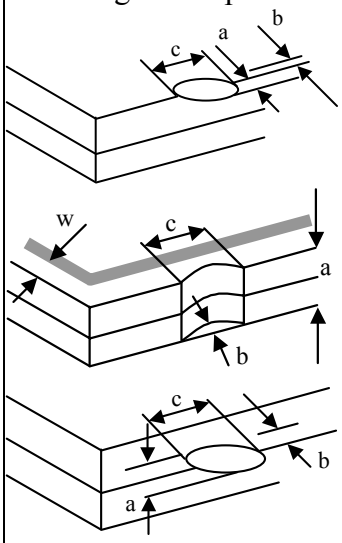
<table>
<tr><th>NO.</th><th>ITEM</th><th>CRITERIA</th></tr>
<tr><td>8</td><td>LINE DEFECT ON DISPLAY</td><td>OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW</td></tr>
<tr><td>9</td><td>MURA ON DISPLAY</td><td>IT'S OK IF MURA IS SLIGHT VISIBLE THROUNG 6% ND FILTER</td></tr>
<tr><td>10</td><td>CF FAIL/SPOT ON DISPLAY</td><td>(1)THE FOLLOWING CF FAIL , SPOT ARE WITHIN THE VIEWING AREA
<table>
<tr><th>SIZE D</th><th>PERMISSIBLE NO.</th></tr>
<tr><td>D ≤ 0.15mm</td><td>IGNORED</td></tr>
<tr><td>0.15mm<D≤0.2mm</td><td>N ≤ 2</td></tr>
<tr><td>D > 0.2mm</td><td>NOT ALLOWED</td></tr>
</table>
Note ：Diameter D=(a+b)/2
 
</td></tr>
<tr><td>11</td><td>UNEVEN COLOR SPREAD , COLORATION</td><td>(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.</td></tr>
<tr><td>12</td><td>BEZEL APPEARANCE</td><td>(1)BEZEL MAY NOT HAVE RUST, E DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION.
(2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.</td></tr>
<tr><td>13</td><td>SOLDERING</td><td>(1)NO SOLDERING FOUND ON THE SPECIFIED PLACE
(2)INSUFFICENT SOLDER

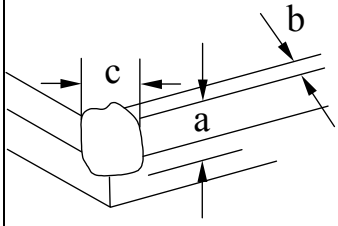
(a)LSI, IC
A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD
 

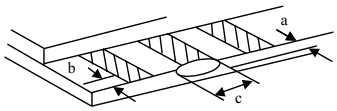
(b)CHIP COMPONENT
· SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING
 
</td></tr>
</table>

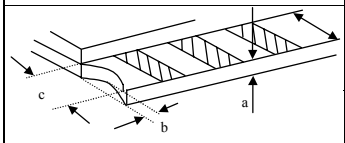
NO.	ITEM	CRITERIA
13.	SOLDERING	• SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED  (3)PARTS ALIGMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE  (b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE 

NO.	ITEM	CRITERIA
13.	SOLDERING	(4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.
14.	BACKLIGHT	(1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
15.	GENERAL APPEARANCE	(1)NO OXIDATION,CONTAMINATION, URVES OR,BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

<table>
<tr><th>NO.</th><th>ITEM</th><th>CRITERIA</th></tr>
<tr><td rowspan="4">16.</td><td rowspan="4">CRACKED GLASS</td><td>THE LCD WITH EXTENSIVE CRACK IS NOT ACCEPTABLE
General glass chip :
 

<table>
<tr><th>a</th><th>b</th><th>c</th></tr>
<tr><td>≤ t/2</td><td>< VIEWING AREA</td><td>≤ 1/8X</td></tr>
<tr><td>t/2 > , ≤ 2t</td><td>≤ W/2</td><td>≤ 1/8X</td></tr>
</table>
*W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE.
X = LCD SIDE LENGTH
t = GLASS THICKNESS</td></tr>
<tr><td>Corner part:
 

<table>
<tr><th>a</th><th>b</th><th>c</th></tr>
<tr><td>≤ t/2</td><td>< VIEWING AREA</td><td>≤ 1/8X</td></tr>
<tr><td>> t/2 , ≤ 2t</td><td>≤ W/2</td><td>≤ 1/8X</td></tr>
</table>
*W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE.
X=LCD SIDE LENGTH
t =GLASS THICKNESS</td></tr>
<tr><td>CHIP ON ELECTRODE PAD
 

<table>
<tr><th>a</th><th>b</th><th>c</th></tr>
<tr><td>≤ t</td><td>≤ 0.5mm</td><td>≤ 1/8X</td></tr>
</table>
* X=LCD SIDE WIDTH
t =GLASS THICKNESS</td></tr>
<tr><td> 

<table>
<tr><th>a</th><th>b</th><th>c</th></tr>
<tr><td>≤ t</td><td>≤ 1/8X</td><td>≤ L</td></tr>
</table>
*X=LCD SIDE WIDTH
t = GLASS THICKNESS
L=ELECTRODE PAD LENGTH
①IF GLASS CHIPPING THE ITO TERMINAL, OVER 2/3 OF THE ITO MUST REMAIN AND BE , INSPECTED ACCORDING TO ELECTRODE TERMINAL SPECIFICATIONS
②IF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DEMAGED</td></tr>
</table>

12.4 RELIABILITY TEST

12.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION
1	High temperature operation	The sample should be allowed to stand at +80°C for 240 hrs
2	Low temperature operation	The sample should be allowed to stand at -30°C for 240 hrs
3	High temperature storage	The sample should be allowed to stand at +80°c for 240 hrs
4	Low temperature storage	The sample should be allowed to stand at -40°C for 240 hrs
5	High temp / humidity test	The sample should be allowed to stand at 60°C , 90% RH 240 hrs
6	Thermal shock (not operated)	The sample should be allowed to stand the following 200 cycles of operation: -25°c for 30 minutes ~ +70°c for 30 minutes
7	ESD (Electrostatic Discharge)	AIR DISCHARGE ± 4KV CONTACT DISCHARGE ± 2KV

NOTE (1) : THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS , THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

12.5 TESTING CONDITIONS AND INSPECTION CRITERIA

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in table 12.5 , standard specifications for reliability have been executed in order to ensure stability .

NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	Current consumption	Refer To Specification	The current consumption should conform to the product specification.
2	Contrast	Refer TO Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
3	Appearance	Visual inspection	Defect free

12.6 OPERATION

12.6.1 Do not connect or disconnect modules to or from the main system while power is being supplied .

12.6.2 Use the module within specified temperature ; lower temperature causes the retardation of blinking speed of the display ; higher temperature makes overall display discolor . When the temperature returns to normality , the display will operate normally .

12.6.3 Adjust the LC driving voltage to obtain the optimum contrast .

12.6.4 Power On Sequence input signals should not be supplied to LCD module before power supply voltage is applied and reaches the specified value . If above sequence is not followed , CMOS LSIs of LCD modules may be damaged due to latch - up problem .

12.7 NOTICE

12.7.1 Use a grounded soldering iron when soldering connector I/O terminals . For soldering or repairing , take precaution against the temperature of the soldering iron and the soldering time to prevent peeling off the through-hole-pad .

12.7.2 Do not disassemble . EDT shall not be held responsible if the module is disassembled and upon the reassembly the module failed .

12.7.3 Do not charge static electricity , as the circuit of this module contains CMOS LSIs. A workman's body should always be static-protected by use ofan ESD STRAP . Working clothes for such personnel should be of static-protected material .

12.7.4 Always ground the electrically-powered driver before using it to install the LCD module. While cleaning the work station by vacuum cleaner, do not bring the sucking mouth near the module ; static electricity of the electrically-powered driver or the vacuum cleaner may destroy the module .

12.7.5 Don’t give external shock.

12.7.6 Don’t apply excessive force on the surface.

12.7.7 Liquid in LCD is hazardous substance. Must not lick and swallow. When the liquid is attach to your, skin, cloth etc. Wash it out thoroughly and immediately.

12.7.8 Don’t operate it above the absolute maximum rating.

12.7.9 Storage in a clean environment, free from dust, active gas, and solvent.

12.7.10 Store without any physical load.

12.7.11 Rewiring: no more than 3 times .